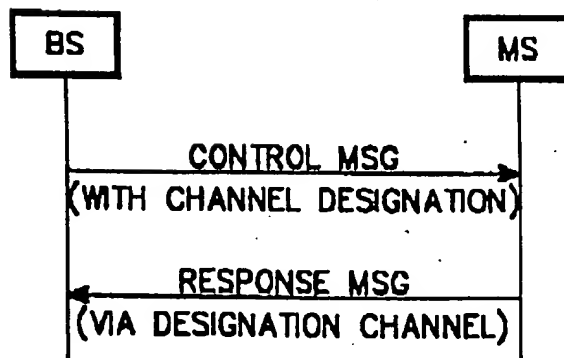


**FIG. 1**  
(PRIOR ART)



**FIG. 2**  
(PRIOR ART)

CONTROL MESSAGE	DESIGNATION ON/OFF	CHANNEL NUMBER	BIT RATE
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FIG. 3

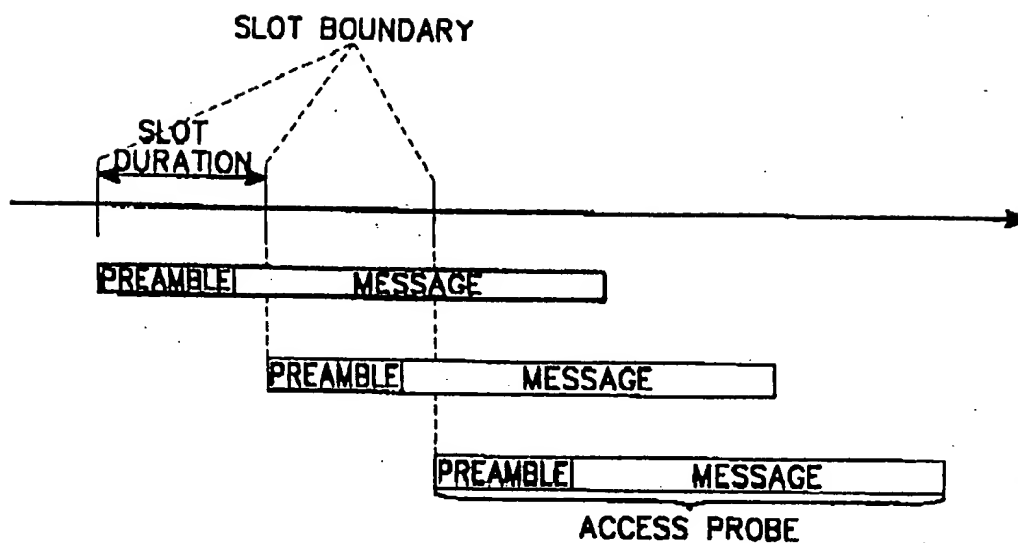


FIG. 4

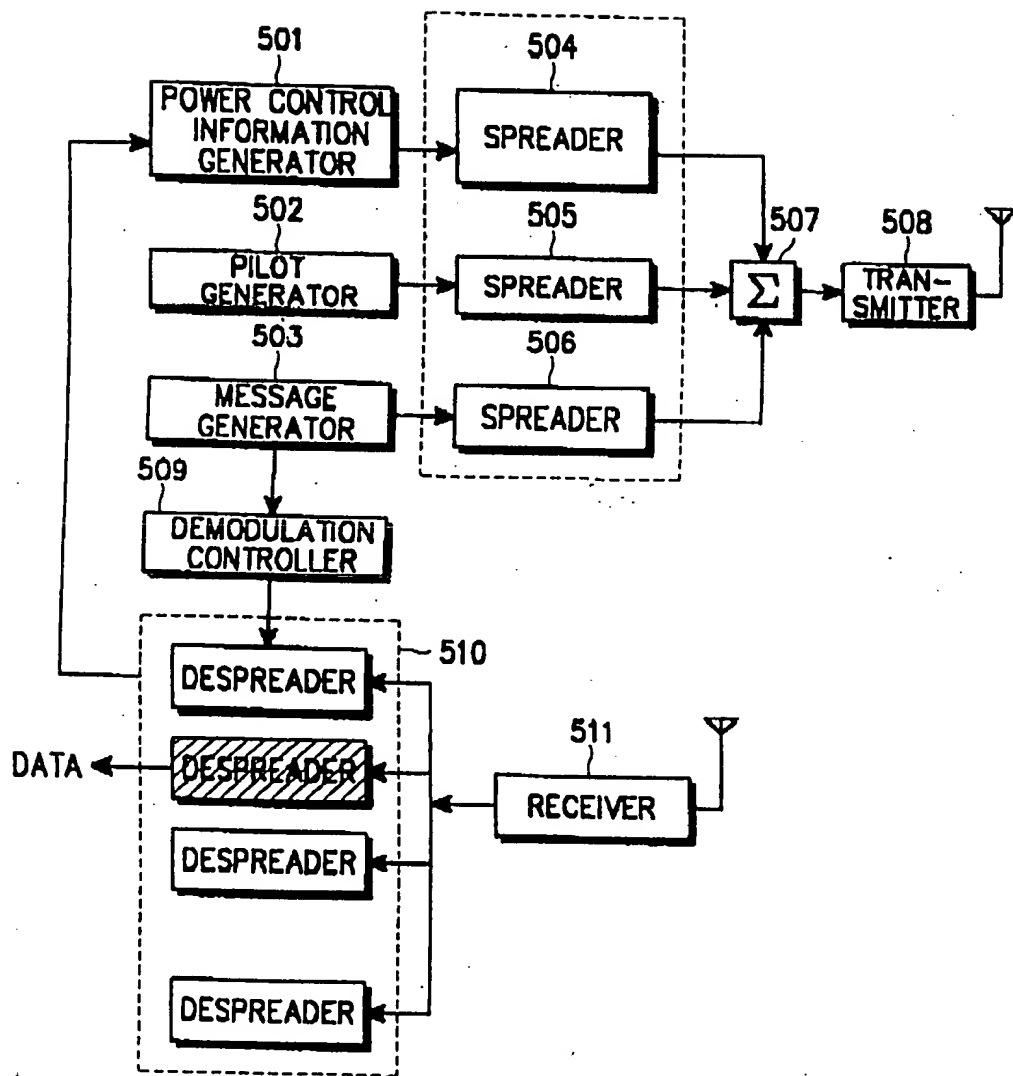
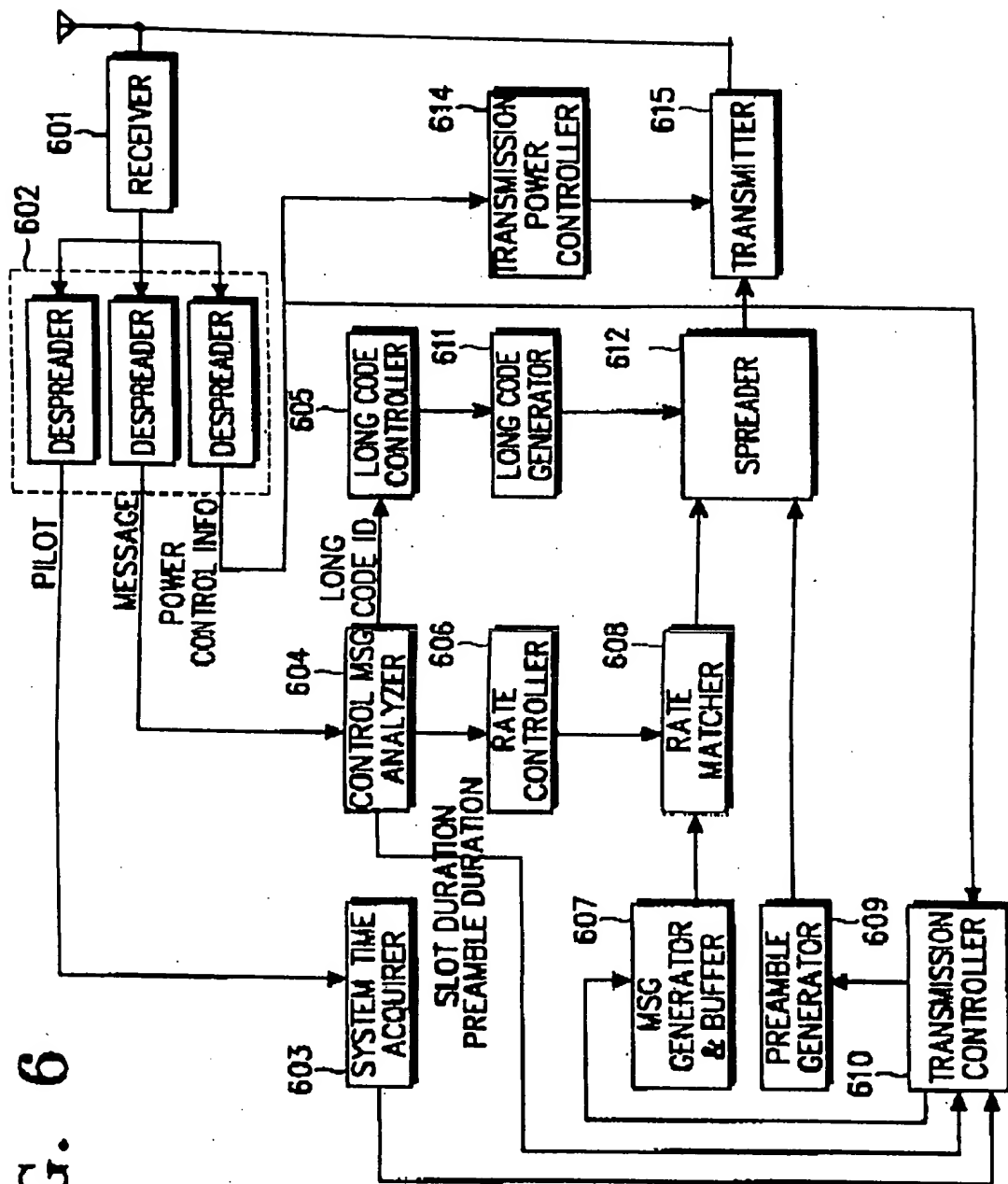


FIG. 5

FIG. 6



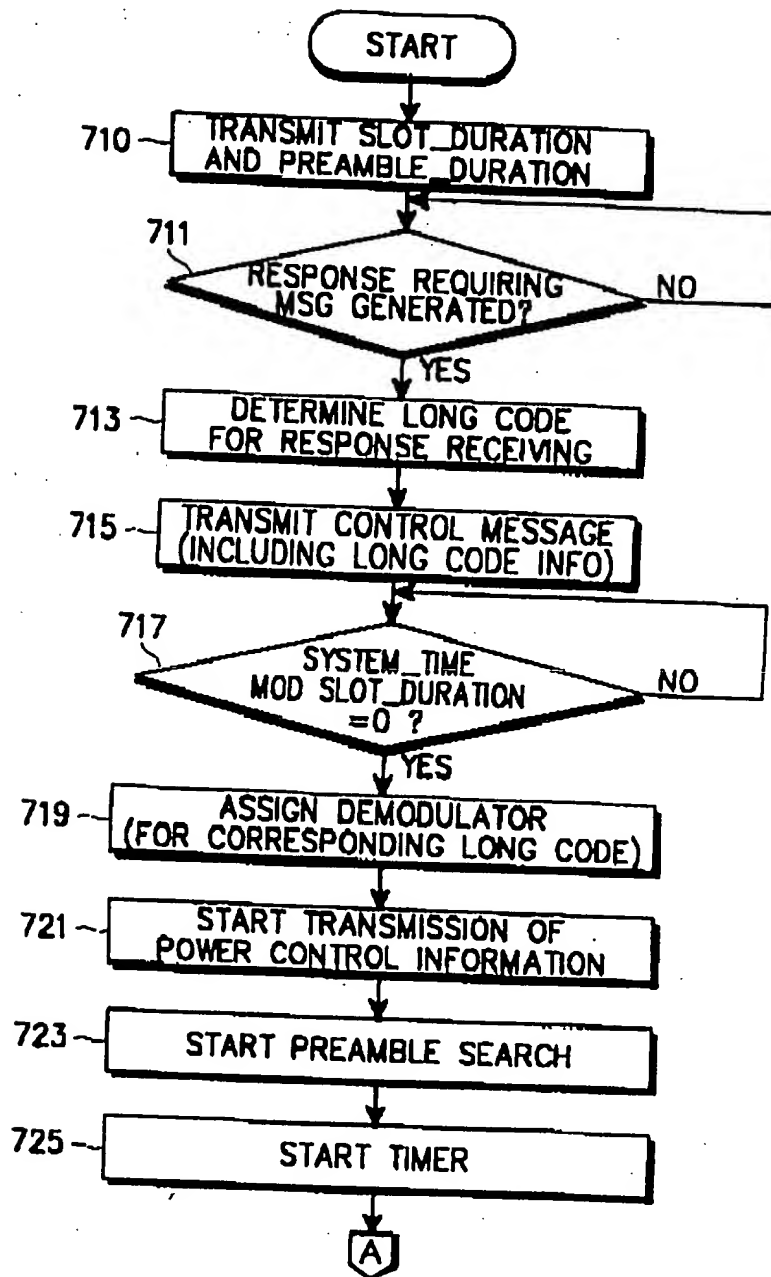


FIG. 7A

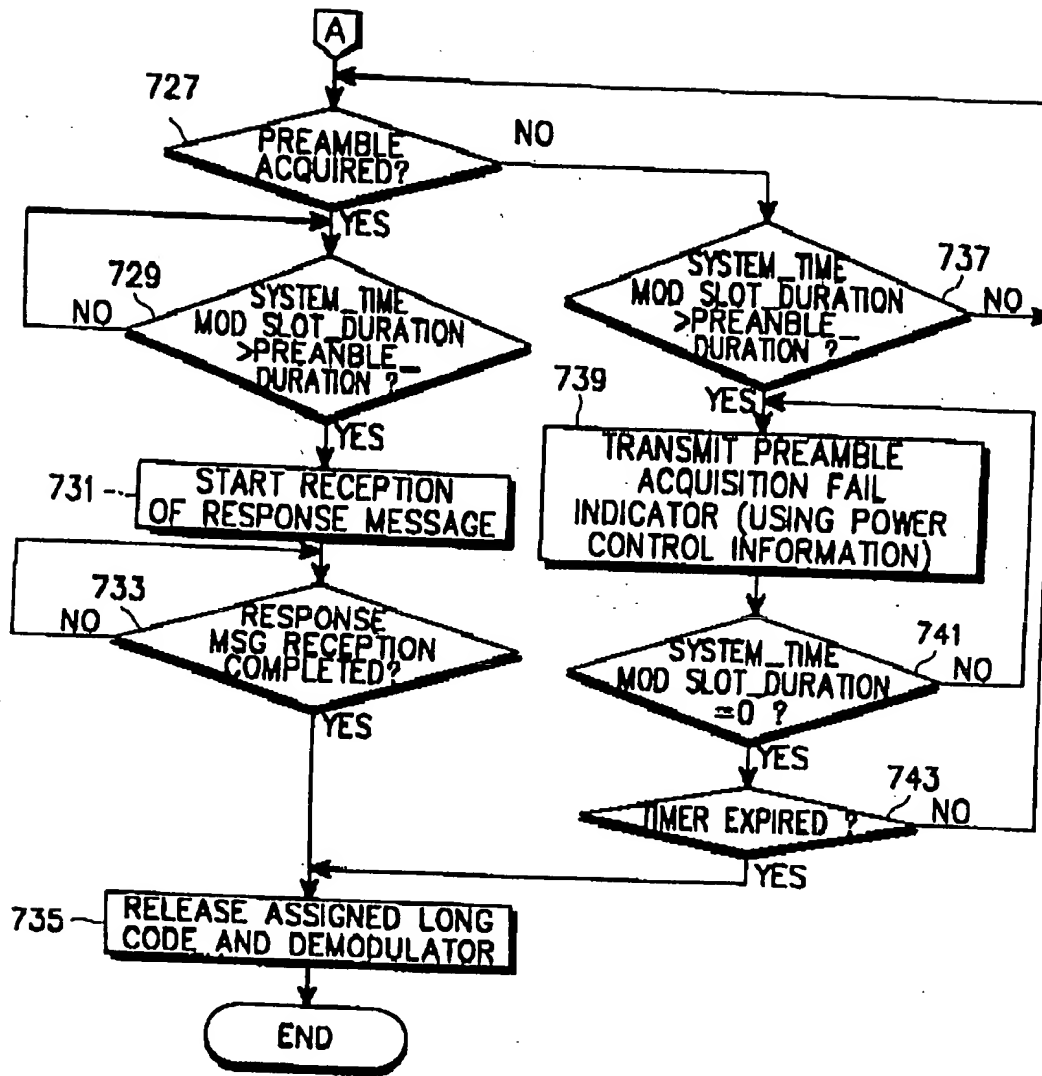


FIG. 7B

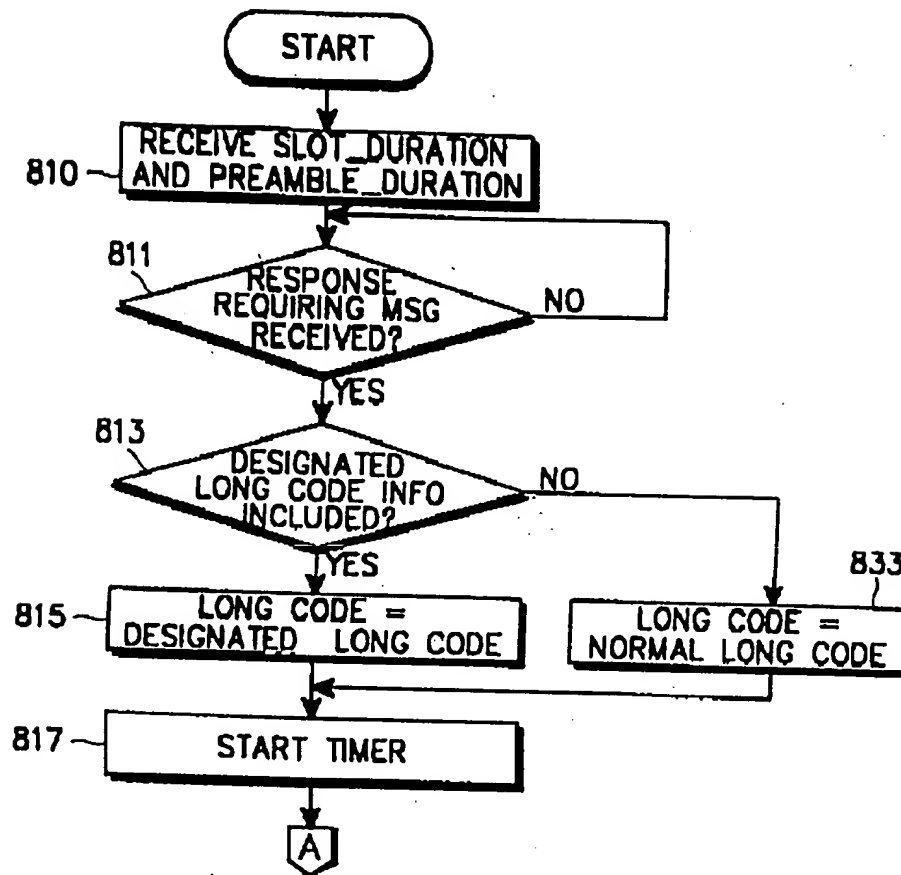


FIG. 8A

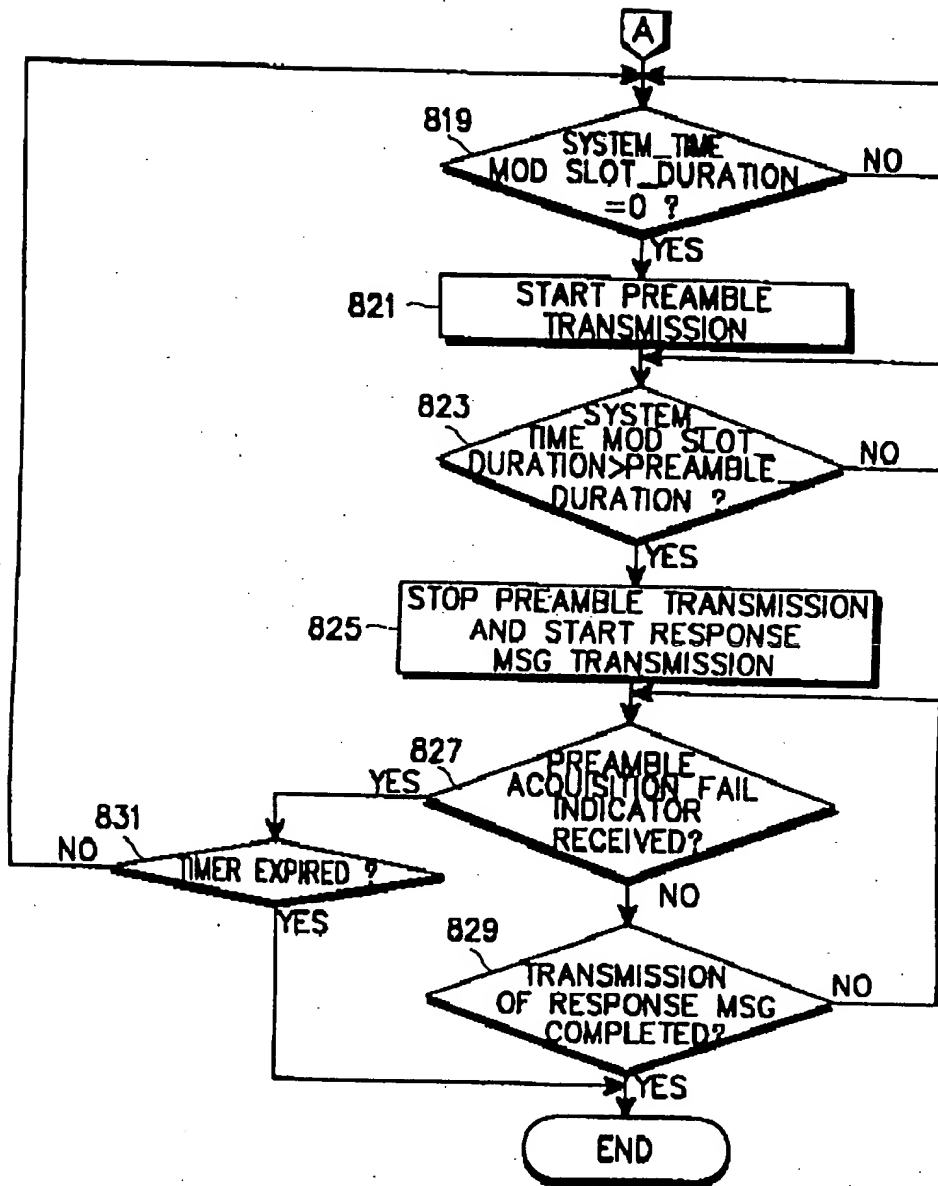


FIG. 8B